

FIG. 1 is a cross-sectional view of a prior art device, showing a substrate 10, a first layer 11, a second layer 12, a third layer 13, a fourth layer 14, and a fifth layer 15.

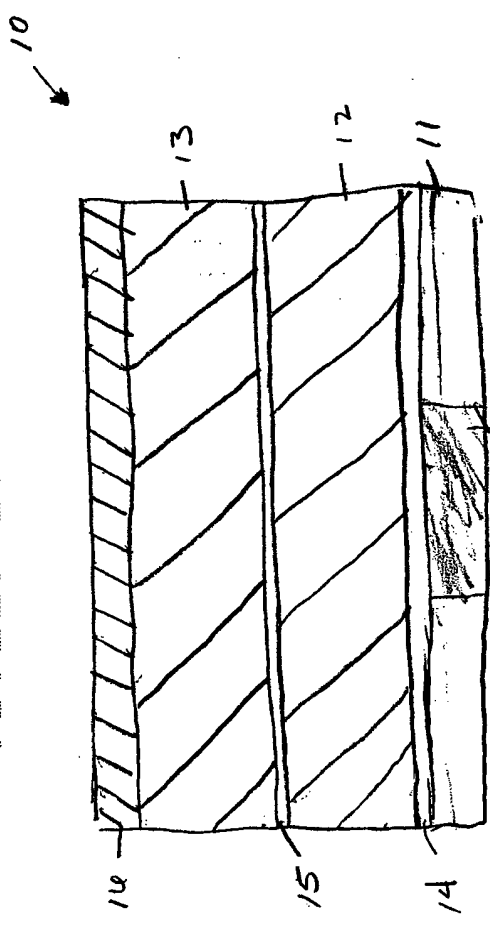


Figure 1
Prior Art

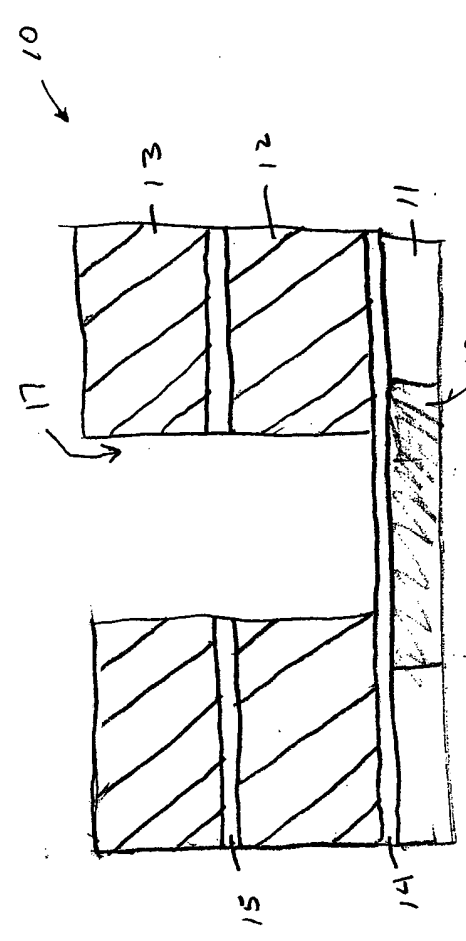


Figure 2
Prior Art

FIG. 3

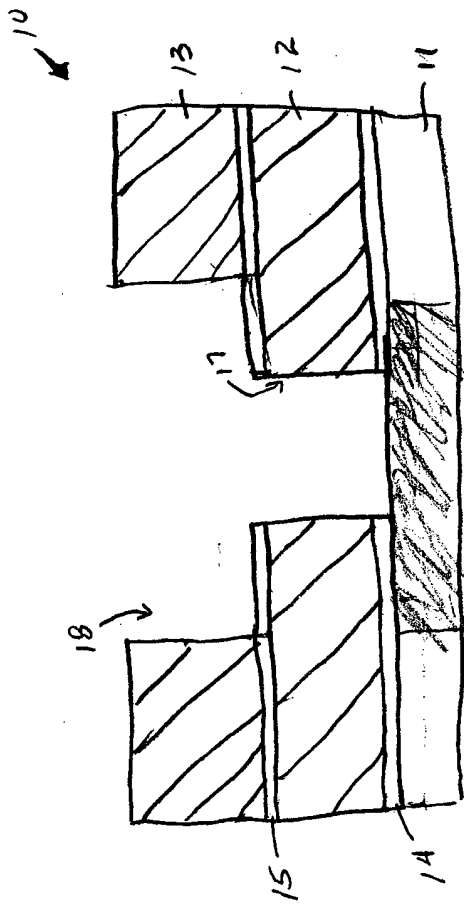


Figure 3
Prior Art

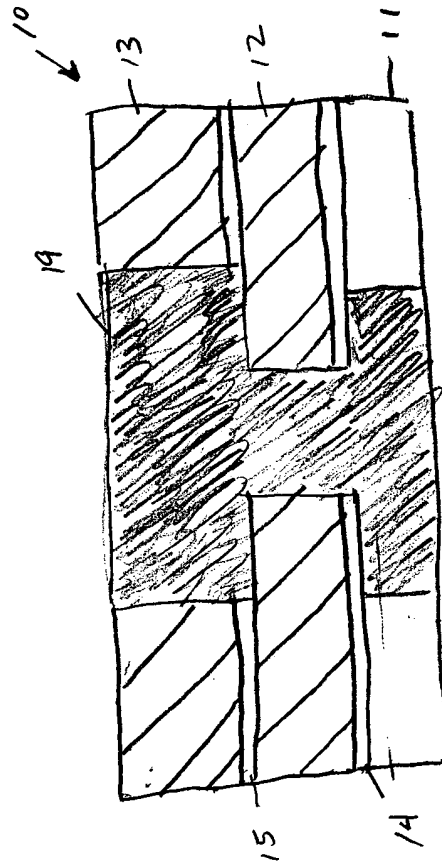


Figure 4
Prior Art

18

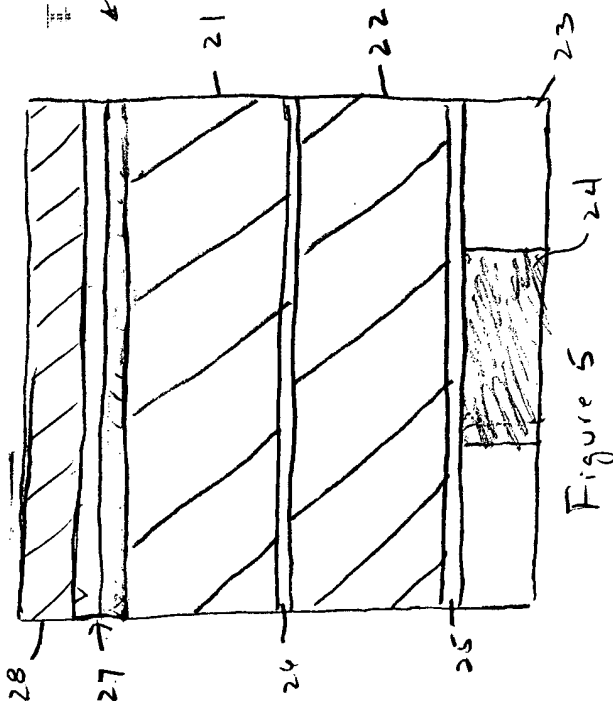


Figure 5
(Prior Art)

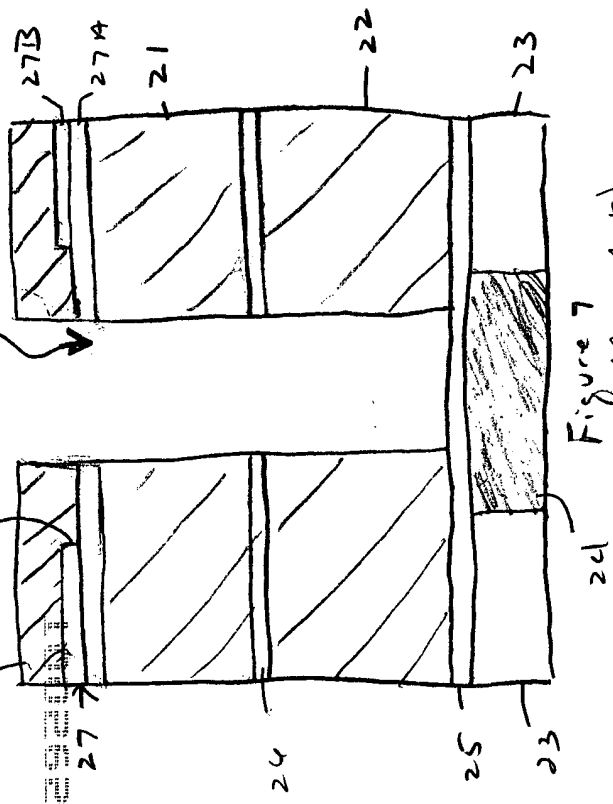


Figure 7
(Prior Art)

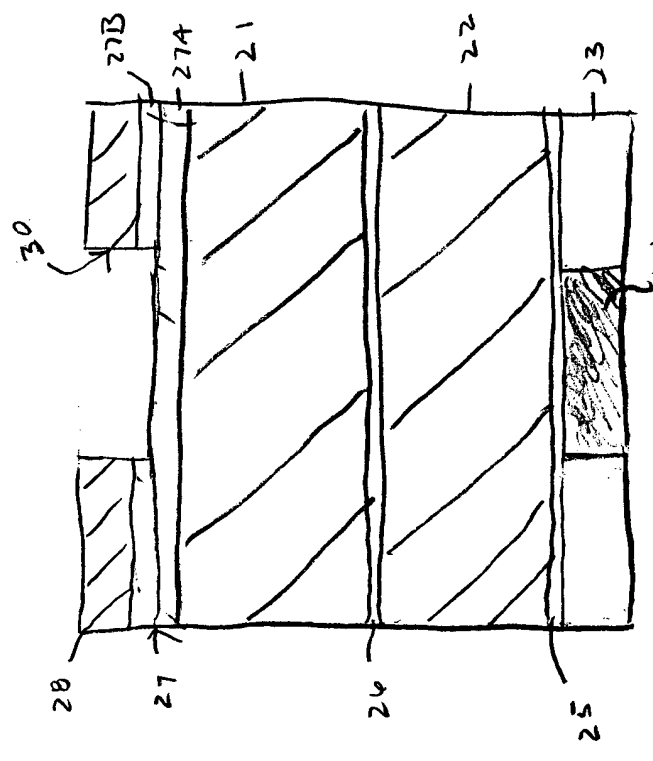


Figure 4
(Prior Art)

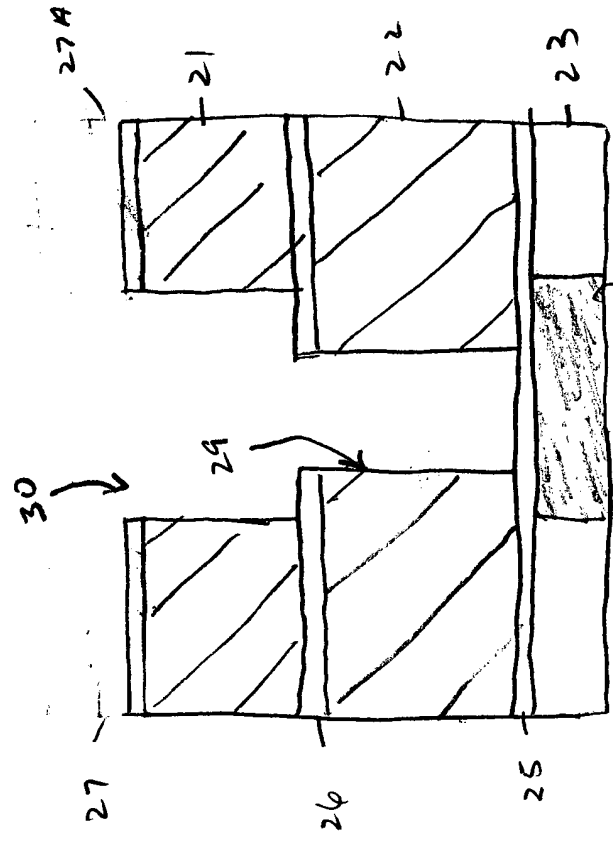


Figure 8
(Prior Art)

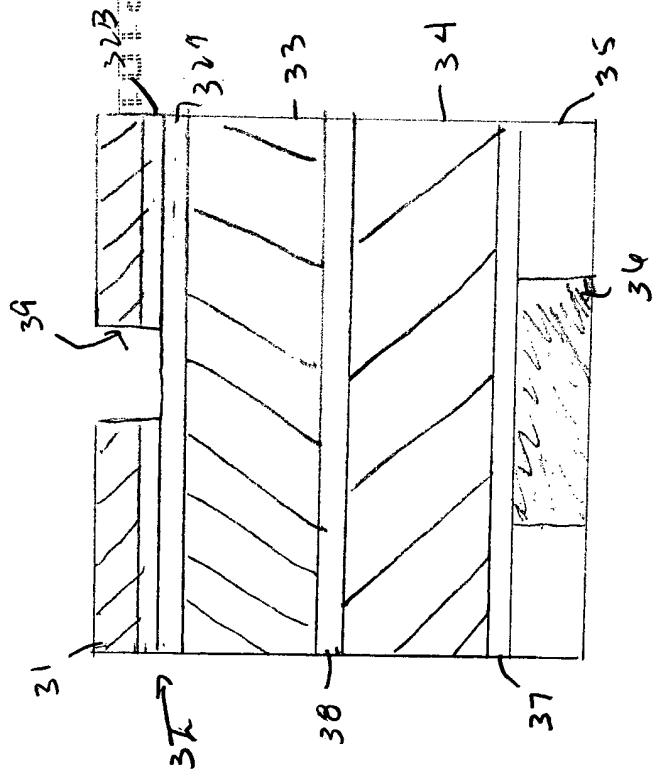


Figure 9
(Prior Art)

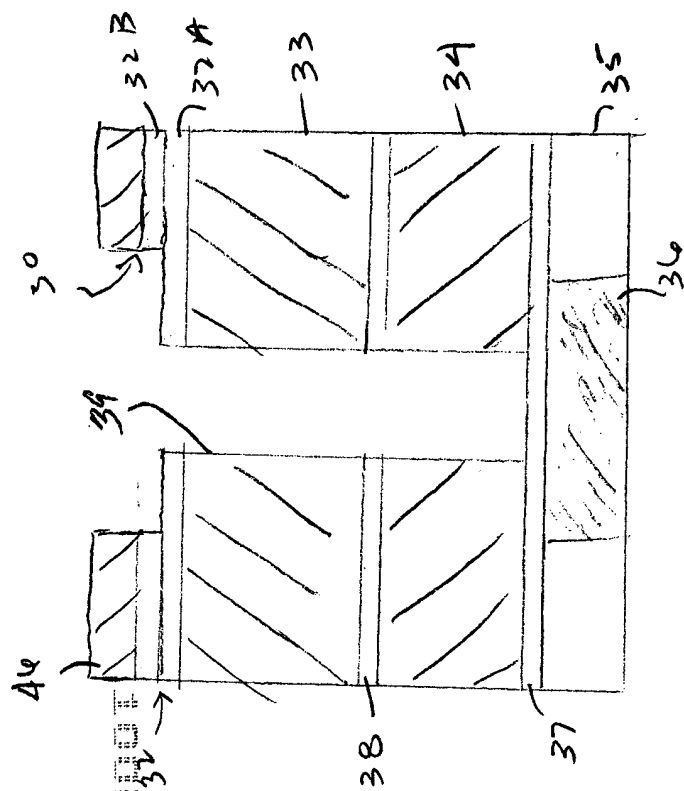


Figure 11
(Prior Art)

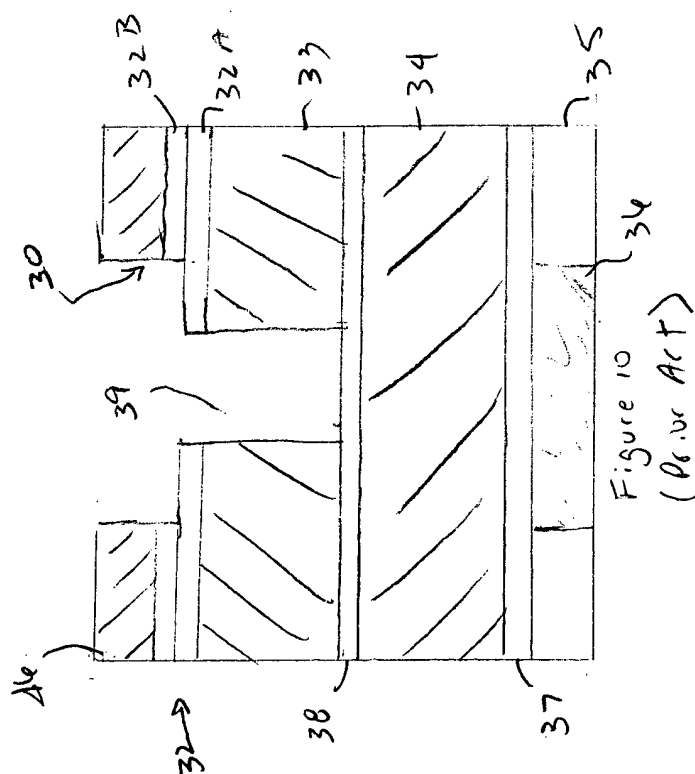


Figure 10
(Prior Art)

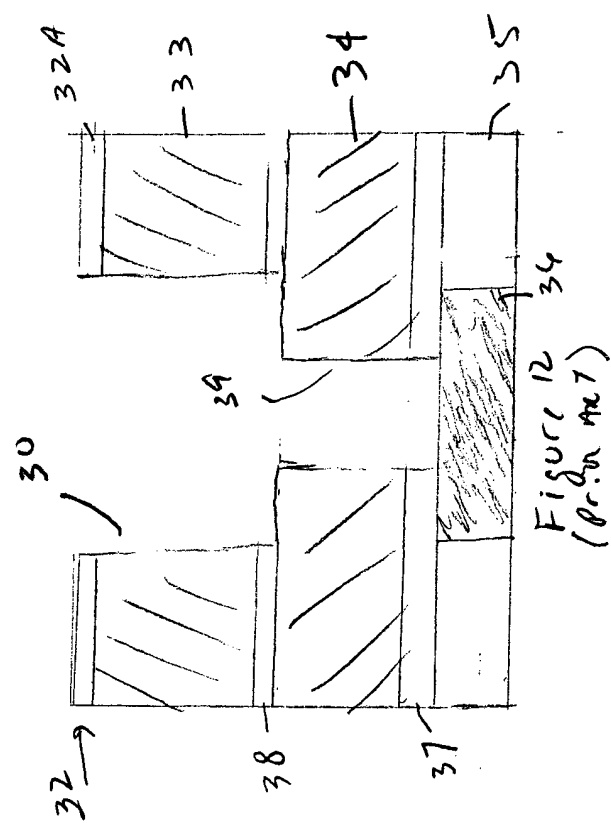


Figure 12
(Prior Art)

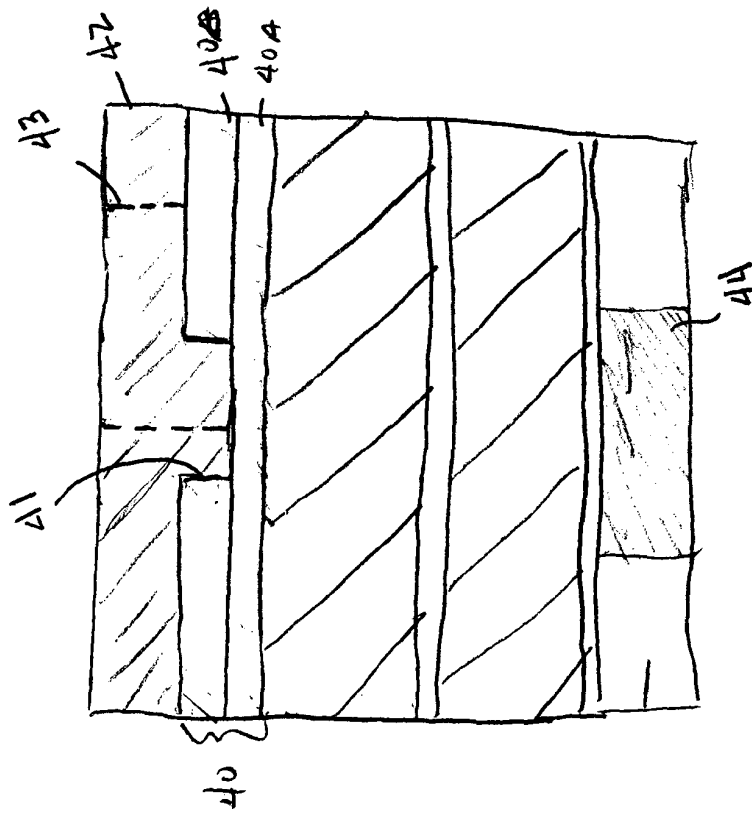


Figure 13

(Prior Art)

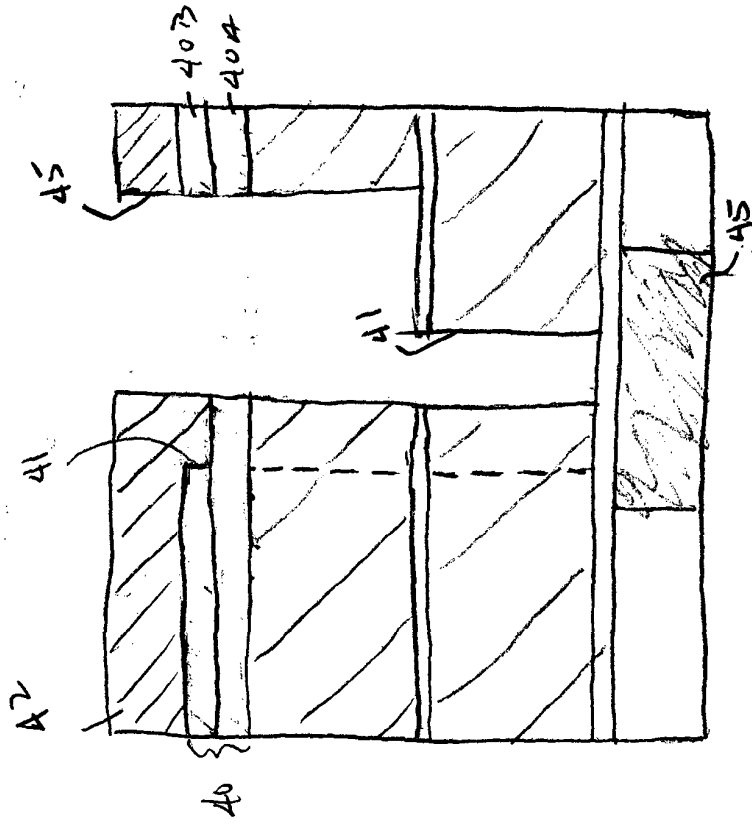


Figure 14

(Prior Art)

FIG. 15

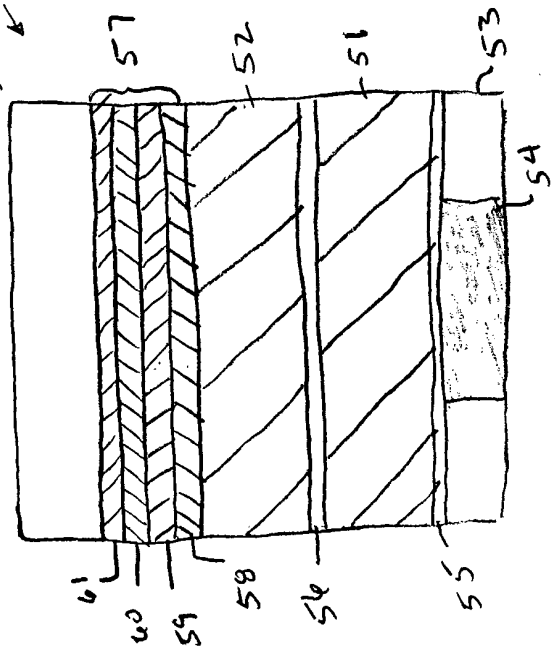


Figure 15

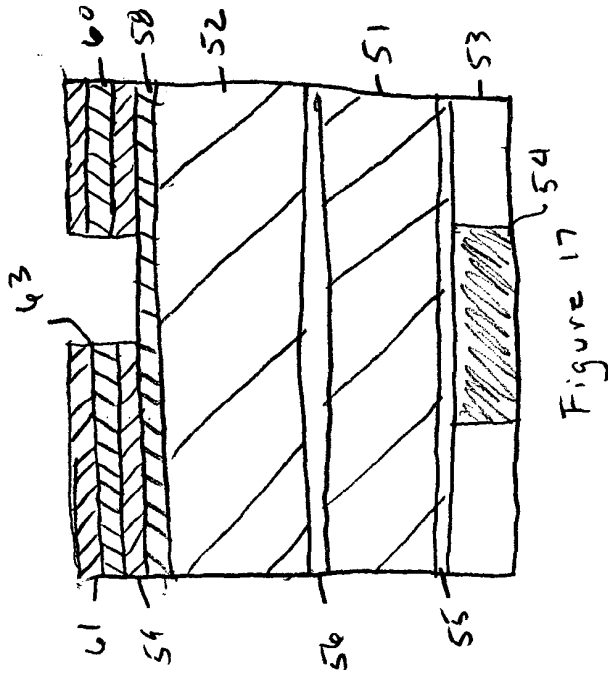


Figure 17

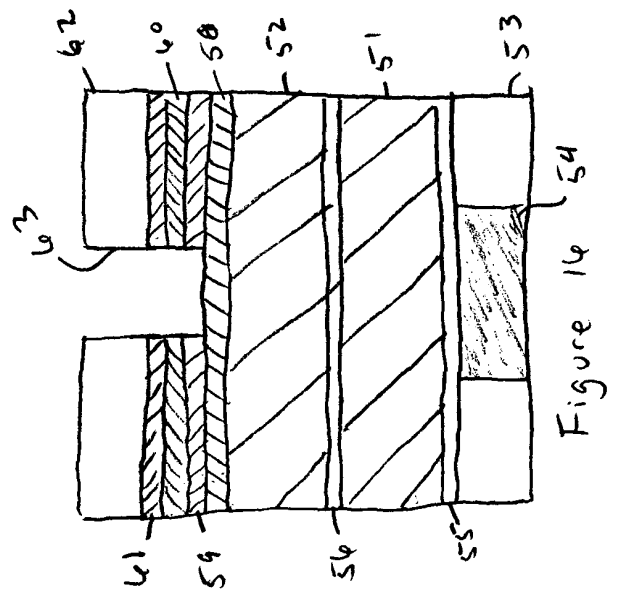


Figure 14

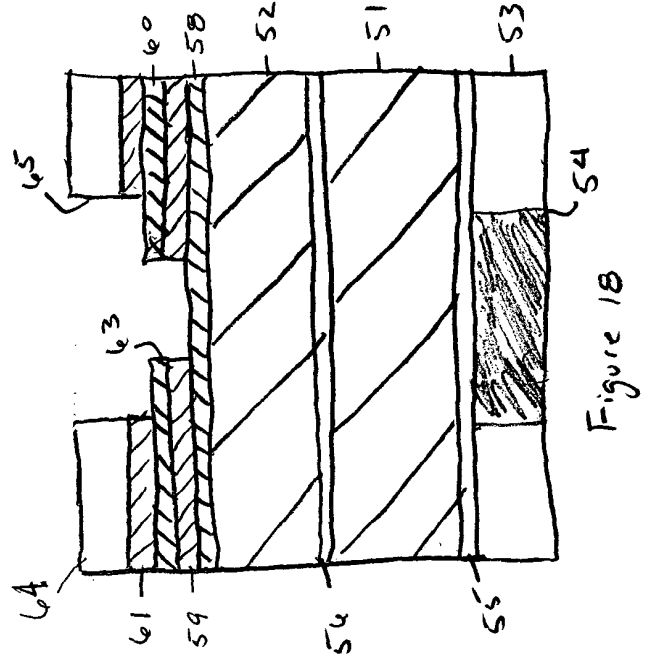


Figure 18

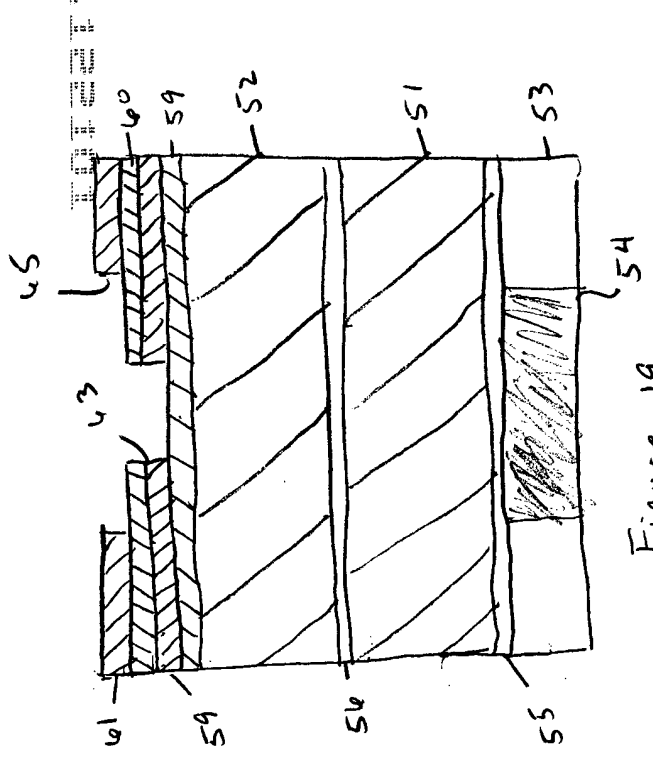


Figure 19

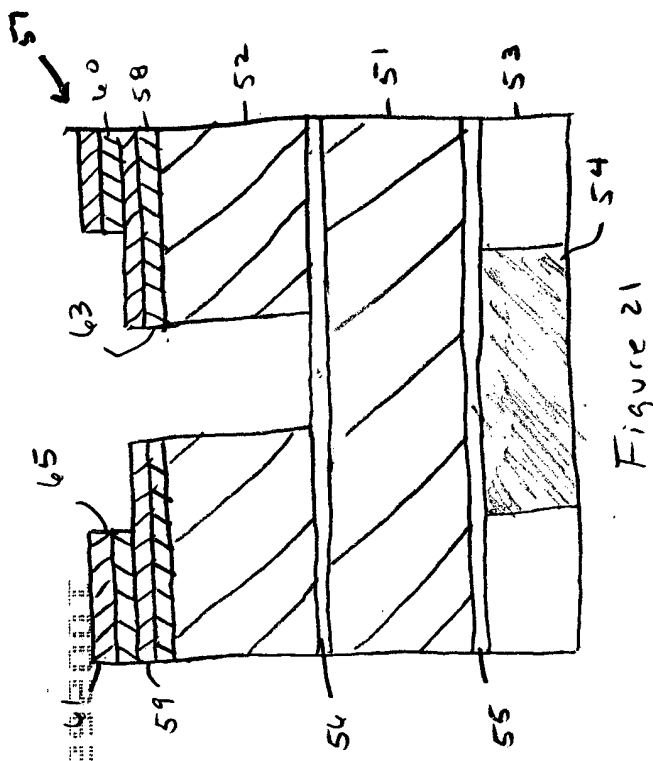


Figure 21

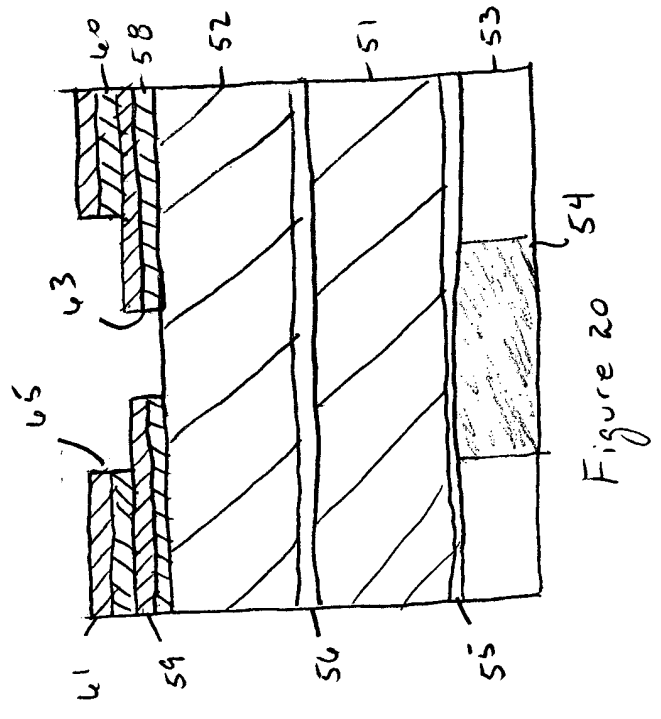


Figure 20

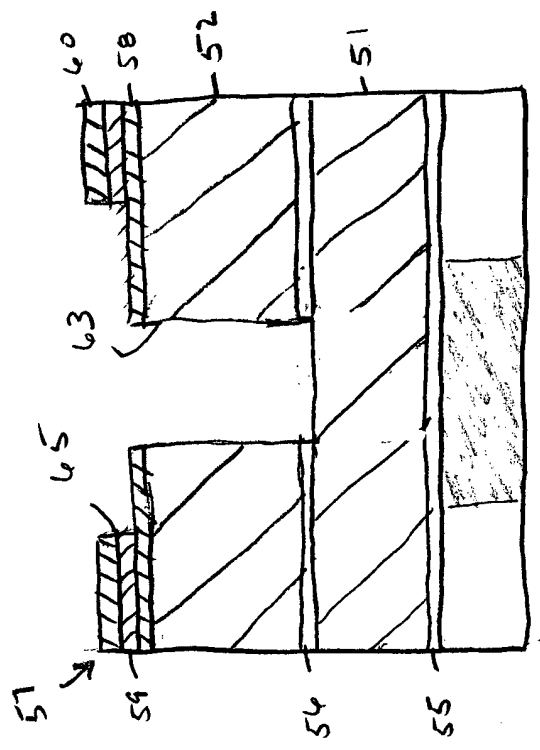


Figure 22

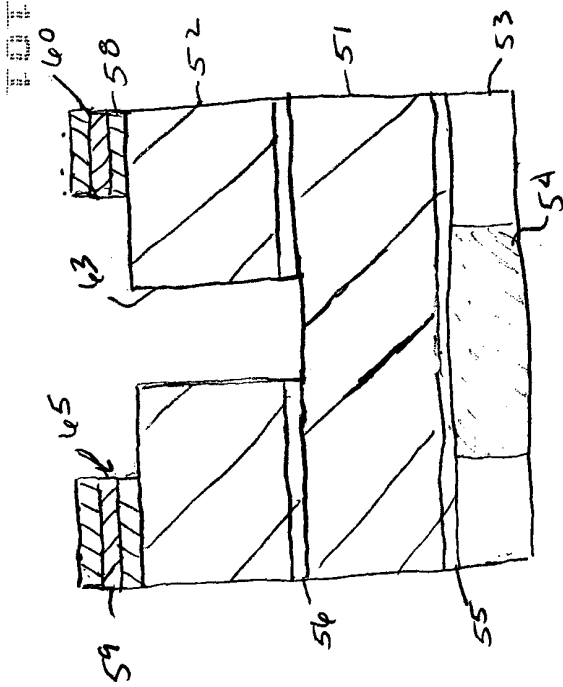


Figure 23

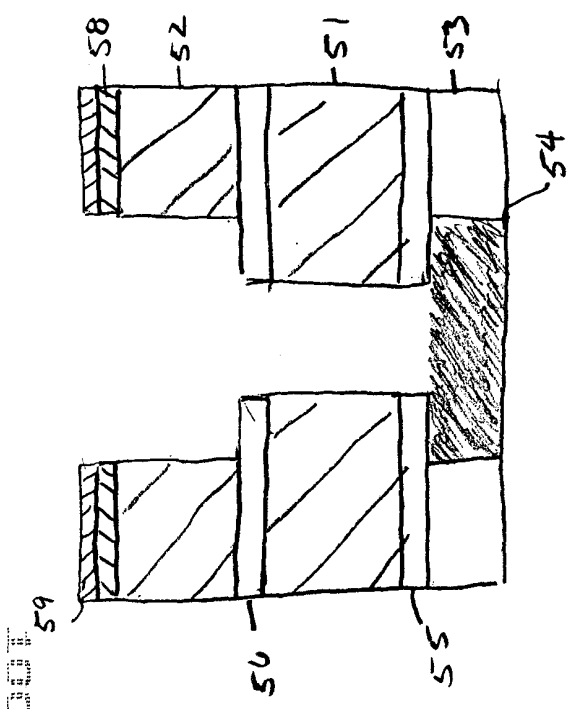


Figure 25

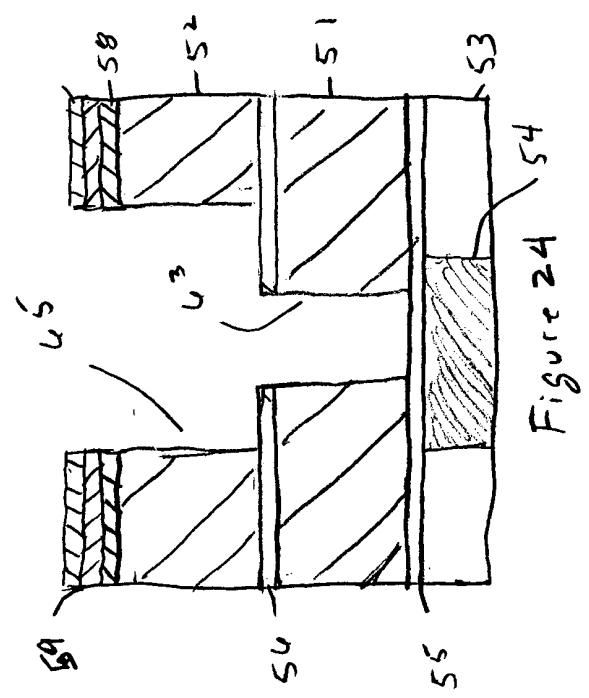


Figure 24

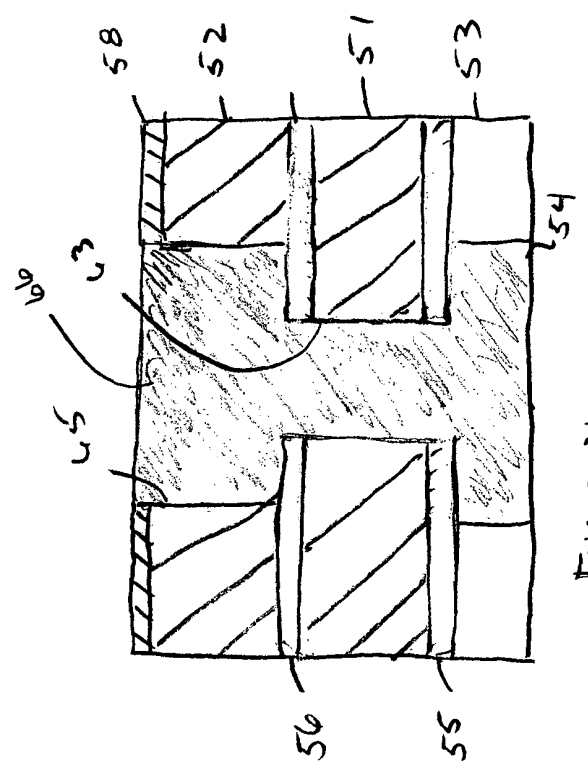


Figure 26

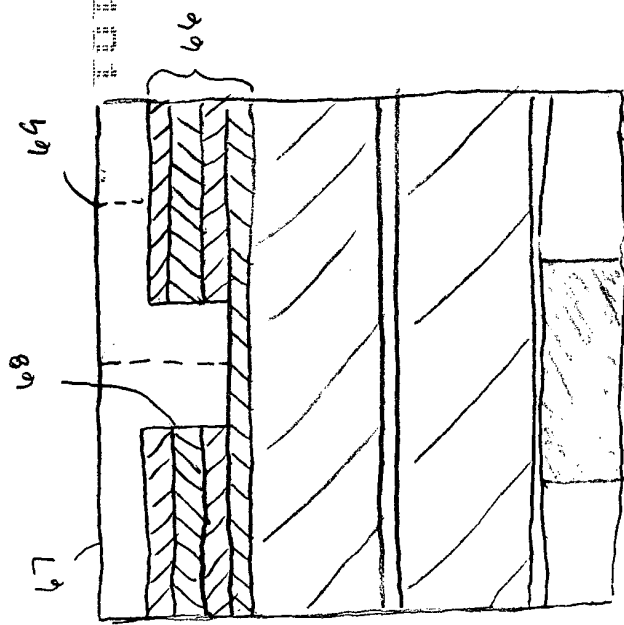


Figure 27

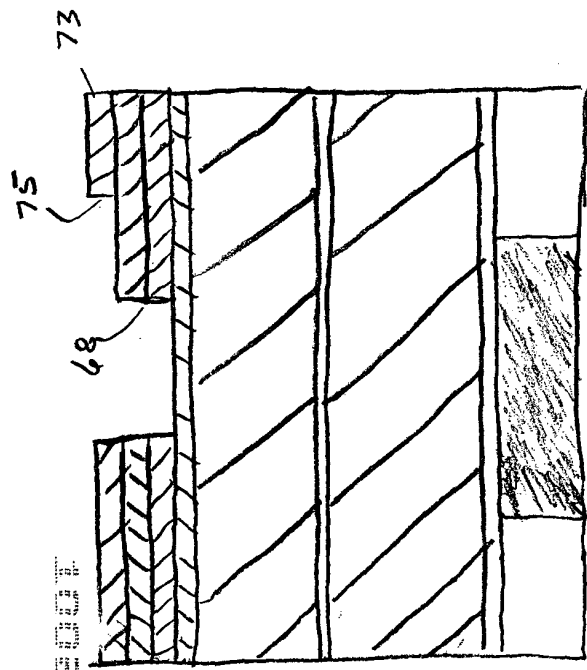


Figure 29

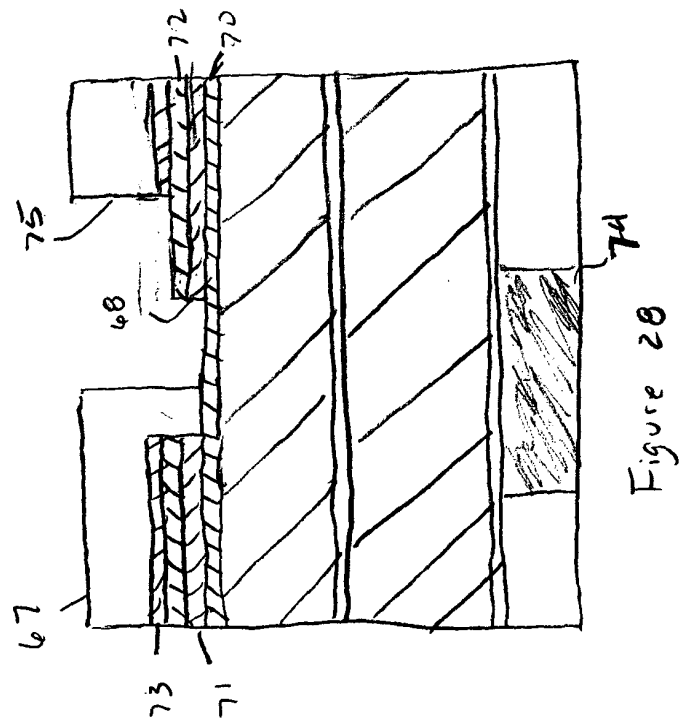


Figure 28